

<b>Notice of Allowability</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	09/671,654	YAMAZAKI ET AL.	
	Examiner Glenn Zimmerman	Art Unit 2879	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to August 9, 2004.
2.  The allowed claim(s) is/are 1-12.
3.  The drawings filed on 29 December 2003 are accepted by the Examiner.
4.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a)  All    b)  Some\*    c)  None    of the:
    1.  Certified copies of the priority documents have been received.
    2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
  - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

#### Attachment(s)

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftsperson's Patent Drawing Review (PTO-948)
3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date August 9, 2003
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application (PTO-152)
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_.

**DETAILED ACTION**

***Response to Amendment***

Amendment, filed on August 9, 2004, has been entered and acknowledged by the examiner.

***Drawings***

The proposed drawing correction and/or the proposed substitute sheets of drawings, filed on December 23, 2003 have been approved.

***Allowable Subject Matter***

Claims 1-12 are allowed.

Regarding claim 1, the following is an examiner's statement of reasons for allowance: The prior art of record neither shows nor suggests an active matrix type organic EL display device including the combination of all the limitations as set forth in claim 1, and specifically wherein the single crystal semiconductor substrate is held in a space which is defined by the bed plate and the cover plate and the packing material could not be found elsewhere in prior art.

Regarding claim 2, claim 2 is allowed for the reasons given in claim 1, because of their dependency status on claim 1.

Regarding claim 3, the following is an examiner's statement of reasons for allowance: The prior art of record neither shows nor suggests an active matrix type organic EL display device including the combination of all the limitations as set forth in claim 3, and specifically wherein the single crystal semiconductor substrate is held in a space which is defined by the bed plate and the cover plate and the packing material could not be found elsewhere in prior art.

Regarding claim 4, claim 4 is allowed for the reasons given in claim 3, because of their dependency status on claim 3.

Regarding claim 5, the following is an examiner's statement of reasons for allowance: The prior art of record neither shows nor suggests an active matrix type organic EL display device including the combination of all the limitations as set forth in claim 5, and specifically wherein the single crystal semiconductor substrate is held in a space which is defined by the bed plate and the cover plate and the packing material could not be found elsewhere in prior art.

Regarding claim 6, claim 6 is allowed for the reasons given in claim 5, because of their dependency status on claim 5.

Regarding claim 7, the following is an examiner's statement of reasons for allowance: The prior art of record neither shows nor suggests an active matrix type organic EL display device including the combination of all the limitations as set forth in claim 7, and specifically wherein the single crystal semiconductor substrate is held in a space which is defined by the bed plate and the cover plate and the binder layer could not be found elsewhere in prior art.

Regarding claim 8, claim 8 is allowed for the reasons given in claim 7, because of their dependency status on claim 7.

Regarding claim 9, the following is an examiner's statement of reasons for allowance: The prior art of record neither shows nor suggests an active matrix type organic EL display device including the combination of all the limitations as set forth in claim 9, and specifically wherein the single crystal semiconductor substrate is held in a space which is defined by the bed plate and the cover plate and the binder layer could not be found elsewhere in prior art.

Regarding claim 10, claim 10 is allowed for the reasons given in claim 9, because of their dependency status on claim 9.

Regarding claim 11, the following is an examiner's statement of reasons for allowance: The prior art of record neither shows nor suggests an active matrix type organic EL display device including the combination of all the limitations as set forth in claim 11, and specifically wherein the single crystal semiconductor substrate is held in a space which is defined by the bed plate and the cover plate and the binder layer could not be found elsewhere in prior art.

Regarding claim 12, claim 12 is allowed for the reasons given in claim 11, because of their dependency status on claim 11.

### ***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Yamazaki et al. U.S. Patent 6,646,288 discloses an Electro-

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Optical Device and Electronic Equipment. Yamazaki U.S. patent 6,531,713 disclose an Electro-Optical Device and Manufacturing Method Thereof. Yamazaki et al. U.S> Patent 6,274,887 disclose a Semiconductor Device and Manufacturing Method Therefor. Yamazaki et al. U.S. Patent 6,440,877 disclose a Method Manufacturing an Electro-Optical Device. Rogers U.S. Patent 6,081,071 disclose an Electroluminescent Apparatus and Methods of Manufacturing and Encapsulating.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Glenn Zimmerman whose telephone number is (571) 272-2466. The examiner can normally be reached on M-W 8-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nimesh D Patel can be reached on (571) 272-2457. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

  
Glenn Zimmerman

  
Vip Patel  
Primary Examiner  
AU 2879